



April 16-18, 2008, Bratislava, Slovakia

<http://www.ui.sav.sk/DDECS2008>

#### General Chair

Michel Renovell  
LIRMM (F)

#### General Vice Chair

Peter Gramata  
Honeywell (SK)

#### Programme Chair

Bernd Straube  
Fraunhofer IIS-EAS (D)

#### Programme Vice-Chair

Miloš Drutarovský  
Technical University of Košice (SK)

#### Publicity Chair

Raimund Ubar  
Tallinn Technical University (EST)

#### Organising Committee Chair

Mária Fischerová  
Institute of Informatics (SK)

#### Steering Committee

T. Garbolino (PL)	A. Pataricza (H)
E. Gramatová (SK)	A. Pawlak (PL)
E. Hryniewicz (PL)	S. Piestrak (F)
Z. Kotásek (CZ)	M. Renovell (F)
A. Kraśniewski (PL)	B. Straube (D)
H. Manhaeve (B)	R. Ubar (EST)
E.J. Marinissen (NL)	K. Vlček (CZ)
O. Novák - chair (CZ)	

Honorary member - A.Hlawiczka (PL)

#### Programme Committee

E.J. Aas (N)	A. Pataricza (H)
J. Becker (D)	A. Pawlak (PL)
D. Borrione (F)	Z. Peng (S)
M. Bucher (GR)	S. Piestrak (F)
G. Carlsson (S)	W. Pleskacz (PL)
K. Chakrabarty (USA)	A. Pleštil (CZ)
S. Chakravarty (USA)	P. Prinetto (I)
M. Daněk (CZ)	J. Raik (EST)
R. Drechsler (D)	M. Renovell (F)
M. Drutarovský (SK)	B. Rouzeyre (F)
L.A. Entrena (E)	G. Russel (UK)
B. Feher (H)	J. Schlöffel (D)
J. Figueras (E)	D. Sciuto (I)
T. Garbolino (PL)	J. Segura (E)
P. Girard (F)	L. Sekanina (CZ)
M. Glesner (D)	O. Sentieys (F)
E. Gramatová (SK)	M. Sonza Reorda (I)
S. Hellebrand (D)	J. Sosnowski (PL)
A. Hlawiczka (PL)	Z. Stamenkovic (D)
E. Hryniewicz (PL)	A. Steinger (A)
L. Jóźwiak (NL)	V. Stopjaková (SK)
Z. Kotásek (CZ)	B. Straube (D)
A. Kraśniewski (PL)	I. Teixeira (P)
K. Kuchcinski (S)	P. Teixeira (P)
C. Landrault (F)	J. Tyszer (PL)
E. Larsson (S)	R. Ubar (EST)
R. Leveugle (F)	M. Udrescu (RO)
H. Manhaeve (B)	H.T. Vierhaus (D)
N. Nicolici (CAN)	K. Vlček (CZ)
F. Novak (SLO)	H.J. Wunderlich (D)
O. Novák (CZ)	Y. Zorian (USA)

#### Organising Committee

E. Gramatová, I. Fodorová, P. Trebatický,  
M. Baláz, P. Malík (SK)

## Call for Papers

The **IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems** provides a forum for exchanging ideas, discussing research results and presenting practical applications in the areas of design, test and diagnosis of microelectronic circuits and systems.

The **DDECS Workshop series** is organised by Central European countries: Czech Republic (1997, 2002, 2006), Poland (1998, 2003, 2007), Slovakia (2000, 2004) and Hungary (2001, 2005). **DDECS 2008** will take place in Bratislava, the capital of the Slovak Republic, that is located on the banks of the river Danube between other capitals - Vienna and Budapest. The Workshop is organised by the Institute of Informatics in Bratislava and sponsored by the Test Technology Technical Council (TTTC) of the IEEE Computer Society.

**Topics of interest** include but are not limited to:

- ASIC/FPGA Design
- Bio-inspired Hardware
- Design Verification/Validation
- Formal Methods in System Design
- Hardware/Software Co-Design
- IP-based Design
- Logic Synthesis
- Physical Design
- Reconfigurable Computing
- System-on-a-Chip (SoC)
- Analog, Mixed-Signal, RF Design and Test
- ATE Hardware and Software
- Built-in Self-Test (BIST)
- Design for Testability and Diagnosis
- Defect/Fault Tolerance and Reliability
- Embedded Test
- Memory and Processor Test
- MEMS Testing

**Submissions:** Prospective authors are cordially invited to submit original papers using the electronic submission on the Workshop web page. Papers should be written in English of 6 pages maximum in the PDF format. Special students and industrial sessions will be organised. Accepted papers will be included in the IEEE Workshop Proceedings.

**Important dates:**

- Submission deadline:	<b>January 13, 2008</b>
- Notification of acceptance:	<b>February 26, 2008</b>
- Camera-ready deadline:	<b>March 16, 2008</b>

#### Further information:

Michel Renovell - General Chair  
Dept. of Microelectronics, LIRMM  
161, Rue Ada  
34392 Montpellier Cedex 5, France  
Tel.: +33 467 418 523  
Fax: +33 467 418 500  
E-mail: [renovell@lirmm.fr](mailto:renovell@lirmm.fr)

Miloš Drutarovský - Programme Vice-Chair  
Dept. of Electronics & Multimedia Communications  
Technical University of Košice  
Park Komenského 13  
04120 Košice, Slovak Republic  
Tel.: + 421 55 602 4169  
Fax: + 421 55 632 3989  
E-mail: [Milos.Drutarovsky@tuke.sk](mailto:Milos.Drutarovsky@tuke.sk)

Bernd Straube - Programme Chair  
Fraunhofer IIS/EAS Dresden  
Zeunerstr. 38  
01069 Dresden, Germany  
Tel.: +49 351 46 40 740  
Fax: +49 351 46 40 703  
E-mail: [Bernd.Straube@eas.iis.fraunhofer.de](mailto:Bernd.Straube@eas.iis.fraunhofer.de)

Mária Fischerová - Organising Chair  
Institute of Informatics  
Slovak Academy of Sciences  
Dúbravská cesta 9  
845 07 Bratislava, Slovak Republic  
Tel.: +421 2 5941 1155  
Fax: +421 2 5941 1161  
E-mail: [maria.fischerova@savba.sk](mailto:maria.fischerova@savba.sk)